

Docket No.: TESSERA 3.0-078 DIV (PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE HONORABLE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Patent Application of: Fjelstad et al.

Application No.: 09/020,647

Group Art Unit: 2827

Filed: February 9, 1998

Examiner: D. Graybill

For :METHODS OF MAKING COMPLIANT SEMICONDUCTOR CHIP PACKAGES

APPELLANTS' REPLY BRIEF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is a reply to the Examiner's Answer mailed on September 11, 2003. This reply is being submitted under the provisions of 37 C.F.R. § 1.193(b)(1).

This Reply Brief is being submitted for the purpose of responding to certain assertions made by the Examiner in his Specifically, on pages 8 and 13 of his Examiner's Answer. Answer, the Examiner contends that Kwon teaches that "elongated bond ribbons extend along the sloping edges of said compliant layer." Moreover, the Examiner contends that FIG. 2 shows the elongated bond ribbons 28. In response, Appellants assert that of а probe view cross-sectional а FIG. is Thus, the metal contact 28 shown in See col. 4, lines 1-3. FIG. 2 is actually conical in shape and substantially fills a via formed in layer 32. Thus, the Examiner's conclusion that metal contact 28 shown in FIG. 2 is the same as or equivalent to an "elongated bond ribbon" is simply not supported or suggested by the Kwon '297 reference.

In view of the above comments and remarks, it is respectfully submitted that the Examiner's position with respect to the obvious nature of the claims is incorrect and Appellants resubmit their position with respect to the patentability of these claims.

If there are any additional charges in connection with this requested Reply Brief, the Examiner is authorized to charge Appellants' Deposit Account No. 12-1095 therefor.

Dated: October 10, 2003

Respectfully submitted,

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